



SGM6503

High Speed, 2x2 SIM Card Bus Crosspoint Switch

GENERAL DESCRIPTION

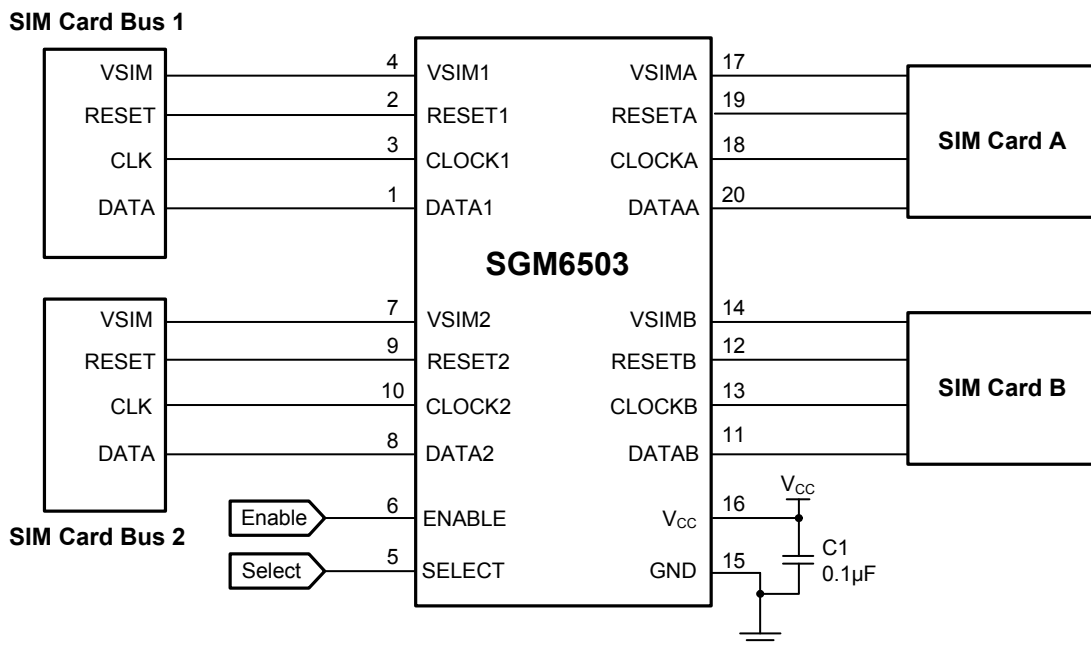
The SGM6503 is a high-speed 2x2 SIM card bus crosspoint switch which is designed to operate from a single 1.8V to 5.5V power supply. It features high-bandwidth (400MHz).

SGM6503 is available in Green TQFN-3x3-20L package.

FEATURES

- Low Voltage Operation: 1.8V to 5.5V
- Low On Capacitance for Data Bus: 12pF
- High -3dB Bandwidth for Data Bus: 400MHz
- Low On Resistance for SIM Supply Bus: 0.65Ω
- High Off-Isolation for Data Bus: -75dB at 1MHz
- Low Crosstalk: -80dB at 1MHz
- Power Supply Current: < 10μA
- TTL/CMOS Compatible
- Available in Green TQFN-3x3-20L Package
- Extended Industrial Temperature Range: -40°C to +85°C

TYPICAL APPLICATION DIAGRAM



PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM6503	TQFN-3x3-20L	-40°C to +85°C	SGM6503YTQG20G/TR	SGM 6503QG XXXXX	Tape and Reel, 3000

NOTE: XXXXX = Date Code and Vendor Code.

Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

V _{CC} to GND	-0.3V to 6.0V
Analog, Digital Voltage Range	-0.3V to V _{CC} + 0.3V
Continuous Current	
NO, NC, or COM (SIM Supply Bus)	200mA
NO, NC, or COM (Data Bus)	30mA
Operating Temperature Range	-40°C to +85°C
Junction Temperature	150°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (Soldering, 10s)	260°C
ESD Susceptibility	
HBM	7000V
MM	400V

NOTE:

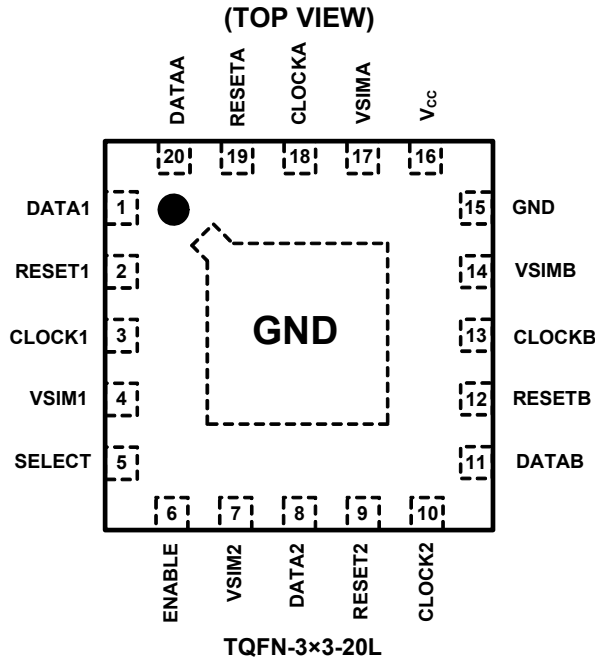
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

CAUTION

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

SGMICRO reserves the right to make any change in circuit design, specification or other related things if necessary without notice at any time. Please contact SGMICRO sales office to get the latest datasheet.

PIN CONFIGURATION



PIN DESCRIPTION

PIN	NAME	FUNCTION
1, 8, 11, 20	DATA1, DATA2, DATAB, DATAA	Data I/O of Data Bus.
2, 9, 12, 19	RESET1, RESET2, RESETB, RESETA	Reset Signal of Data Bus.
3, 10, 13, 18	CLOCK1, CLOCK2, CLOCKB, CLOCKA	Clock Signal of Data Bus.
4, 7, 14, 17	VSIM1, VSIM2, VSIMB, VSIMA	SIM Supply Bus.
5	SELECT	Bus Switching Control.
6	ENABLE	Enable = "0": chip is disabled; input and output are isolated. Enable = "1": bus input and output are connected according to the logic input of "select" pin.
15	GND	Ground.
16	V _{cc}	Power Supply.
Exposed Pad	GND	Exposed pad should be soldered to PCB board and connected to GND or left floating.

FUNCTION TABLE

ENABLE	SELECT	FUNCTION
0	X	Chip is disabled.
1	0	Bus 1 is connected to Bus A; Bus 2 is connected to Bus B.
1	1	Bus 1 is connected to Bus B; Bus 2 is connected to Bus A.

1. Bus 1 is connected to Bus A; Bus 2 is connected to Bus B:

DATA1 = DATAA; RESET1 = RESETA; CLOCK1 = CLOCKS; VSIM1 = VSIMA
DATA2 = DATAB; RESET2 = RESETB; CLOCK2 = CLOCKS; VSIM2 = VSIMB

2. Bus 1 is connected to Bus B; Bus 2 is connected to Bus A:

DATA1 = DATAB; RESET1 = RESETB; CLOCK1 = CLOCKS; VSIM1 = VSIMB
DATA2 = DATAA; RESET2 = RESETA; CLOCK2 = CLOCKS; VSIM2 = VSIMA

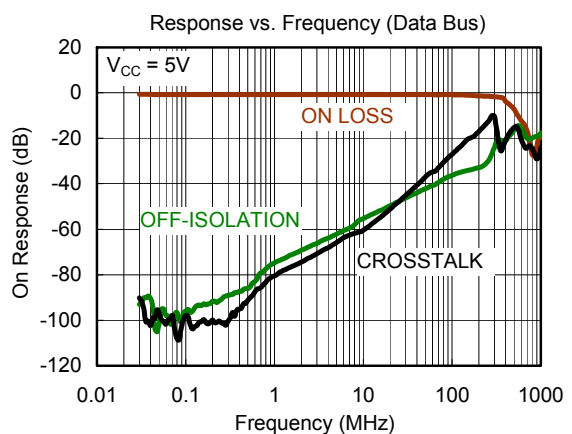
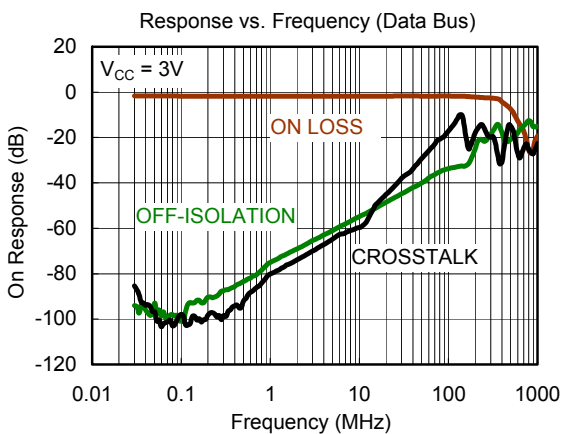
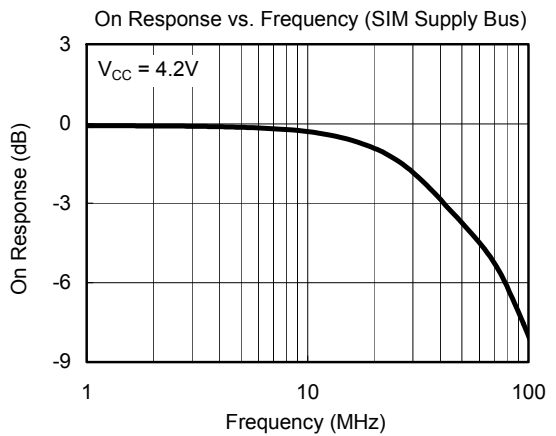
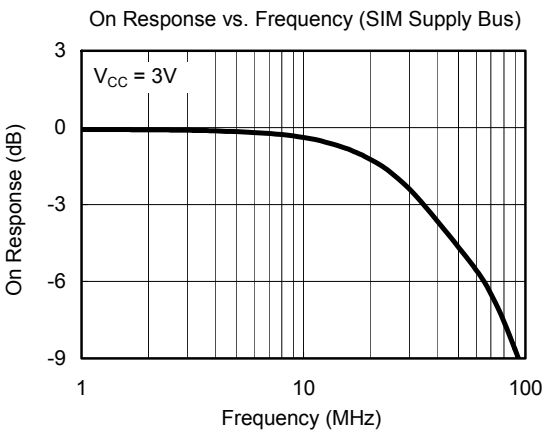
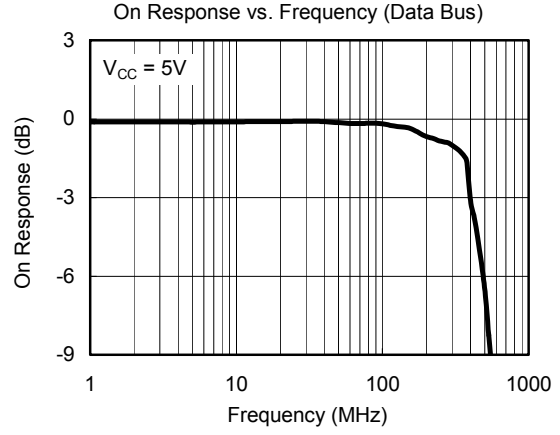
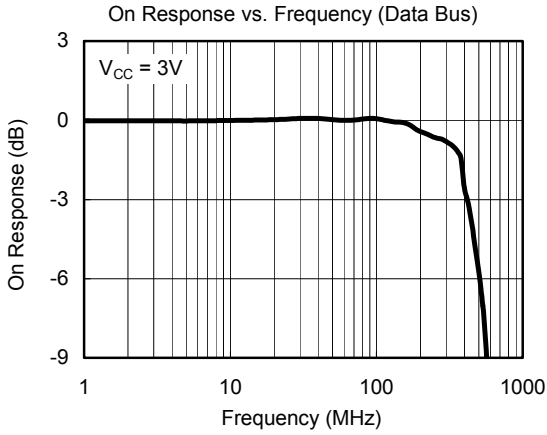
ELECTRICAL CHARACTERISTICS OF DATA BUS SWITCHES(Full = -40°C to +85°C, typical values are at $T_A = +25^\circ\text{C}$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
POWER REQUIREMENTS							
Power Supply Range	V_{CC}		Full	1.8		5.5	V
Power Supply Current	I_{CC}	$V_{CC} = 5.5\text{V}$, $V_{IN} = 0\text{V}$ or V_{CC}	Full			10	μA
DIGITAL INPUTS							
Input High Voltage	V_{INH}	$V_{CC} = 5.0\text{V}$	Full	1.6			V
Input Low Voltage	V_{INL}	$V_{CC} = 5.0\text{V}$	Full			0.5	V
Input Leakage Current	I_{IN}	$V_{CC} = 5.5\text{V}$, $V_{IN} = 0\text{V}$ or 5.5V	Full			1	μA
ANALOG SWITCH							
Source OFF Leakage Current	$I_{NC(OFF)}$, $I_{NO(OFF)}$	$V_{CC} = 5.5\text{V}$, V_{NO} or $V_{NC} = 3.3\text{V}/0.3\text{V}$, $V_{COM} = 0.3\text{V}/3.3\text{V}$	Full			1	μA
Channel ON Leakage Current	$I_{NC(ON)}$, $I_{NO(ON)}$, $I_{COM(ON)}$	$V_{CC} = 5.5\text{V}$, $V_{COM} = 0.3\text{V}/3.3\text{V}$, V_{NO} or $V_{NC} = 0.3\text{V}/3.3\text{V}$ or floating	Full			1	μA
Analog Signal Range	V_{NO} , V_{NC} , V_{COM}		Full	0		V_{CC}	V
On-Resistance	R_{ON}	$V_{CC} = 4.5\text{V}$, $0\text{V} \leq V_{NO}$ or $V_{NC} \leq V_{CC}$, $I_{COM} = -10\text{mA}$, Test Circuit 1	+25°C		12	20	Ω
			Full			23	
On-Resistance Match Between Channels	ΔR_{ON}	$V_{CC} = 4.5\text{V}$, $0\text{V} \leq V_{NO}$ or $V_{NC} \leq V_{CC}$, $I_{COM} = -10\text{mA}$, Test Circuit 1	+25°C		2	3.15	Ω
			Full			3.85	
On-Resistance Flatness	$R_{FLAT(ON)}$	$V_{CC} = 4.5\text{V}$, $0\text{V} \leq V_{NO}$ or $V_{NC} \leq V_{CC}$, $I_{COM} = -10\text{mA}$, Test Circuit 1	+25°C		3.5	5.25	Ω
			Full			5.9	
DYNAMIC CHARACTERISTICS ($V_{CC} = 3.0\text{V}$)							
Turn-On Time	t_{ON}	V_{NC} or $V_{NO} = 1.5\text{V}$, $R_L = 300\Omega$, $C_L = 35\text{pF}$, Test Circuit 2	+25°C		46		ns
Turn-Off Time	t_{OFF}				35		
Break-Before-Make Time Delay	t_D	V_{NC} or $V_{NO} = 1.5\text{V}$, $R_L = 300\Omega$, $C_L = 35\text{pF}$, Test Circuit 3	+25°C		12		ns
Charge Injection	Q	$V_G = \text{GND}$, $R_G = 0\Omega$, $C_L = 0.1\text{nF}$, $Q = C_L \times V_{OUT}$, Test Circuit 4	+25°C		3		pC
Off Isolation	O_{ISO}	Signal = 0dBm, $R_L = 50\Omega$, Test Circuit 5	f = 1MHz	+25°C		-75	dB
			f = 10MHz	+25°C		-55	
Channel-to-Channel Crosstalk	X_{TALK}	Signal = 0dBm, $R_L = 50\Omega$, Test Circuit 6	f = 1MHz	+25°C		-80	dB
			f = 10MHz	+25°C		-60	
-3dB Bandwidth	BW	Signal = 0dBm, $R_L = 50\Omega$, Test Circuit 7	+25°C		400		MHz
Channel ON Capacitance	$C_{NC(ON)}$, $C_{NO(ON)}$, $C_{COM(ON)}$		+25°C		12		pF

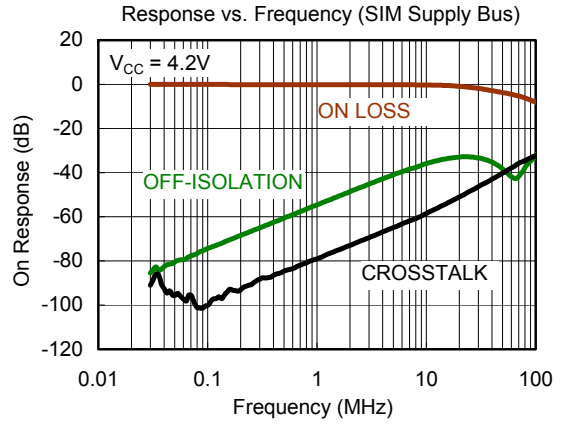
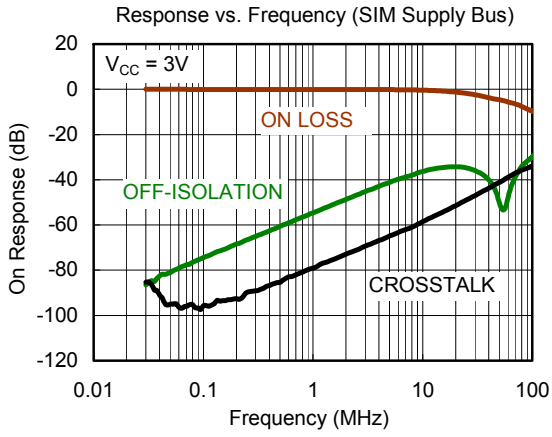
ELECTRICAL CHARACTERISTICS OF SIM SUPPLY BUS SWITCHES(Full = -40°C to +85°C, typical values are at $T_A = +25^\circ\text{C}$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
POWER REQUIREMENTS							
Power Supply Range	V_{CC}		Full	1.8		5.5	V
Power Supply Current	I_{CC}	$V_{CC} = 5.5\text{V}$, $V_{IN} = 0\text{V}$ or V_{CC}	Full			10	μA
DIGITAL INPUTS							
Input High Voltage	V_{INH}	$V_{CC} = 5.0\text{V}$	Full	1.6			V
Input Low Voltage	V_{INL}	$V_{CC} = 5.0\text{V}$	Full			0.5	V
Input Leakage Current	I_{IN}	$V_{CC} = 5.5\text{V}$, $V_{IN} = 0\text{V}$ or 5.5V	Full			1	μA
ANALOG SWITCH							
Source OFF Leakage Current	$I_{NC(OFF)}$, $I_{NO(OFF)}$	$V_{CC} = 4.2\text{V}$, V_{NO} or $V_{NC} = 3.3\text{V}/1.8\text{V}$, $V_{COM} = 1.8\text{V}/3.3\text{V}$	Full			1	μA
Channel ON Leakage Current	$I_{NC(ON)}$, $I_{NO(ON)}$, $I_{COM(ON)}$	$V_{CC} = 4.2\text{V}$, $V_{COM} = 1.8\text{V}/3.3\text{V}$, V_{NO} or $V_{NC} = 1.8\text{V}/3.3\text{V}$ or floating	Full			1	μA
Analog Signal Range	V_{NO} , V_{NC} , V_{COM}		Full	1.8		V_{CC}	V
On-Resistance	R_{ON}	$V_{CC} = 3.3\text{V}$, V_{NO} or $V_{NC} = 2.8\text{V}$ or 1.8V , $I_{COM} = -100\text{mA}$, Test Circuit 1	V_{NO} or $V_{NC} = 2.8\text{V}$	+25°C	0.65	1.05	Ω
				Full		1.25	
			V_{NO} or $V_{NC} = 1.8\text{V}$	+25°C	1.1	1.6	
				Full		1.8	
On-Resistance Match Between Channels	ΔR_{ON}	$V_{CC} = 3.3\text{V}$, V_{NO} or $V_{NC} = 2.8\text{V}$ or 1.8V , $I_{COM} = -100\text{mA}$, Test Circuit 1		+25°C	0.15	0.32	Ω
				Full		0.38	
On-Resistance Flatness	$R_{FLAT(ON)}$	$V_{CC} = 3.3\text{V}$, V_{NO} or $V_{NC} = 1.8\text{V}$, 2.8V , $I_{COM} = -100\text{mA}$, Test Circuit 1		+25°C	0.45	0.63	Ω
				Full		0.69	
DYNAMIC CHARACTERISTICS ($V_{CC} = 3.0\text{V}$)							
Turn-On Time	t_{ON}	V_{NO} or $V_{NC} = 2.0\text{V}$, $R_L = 50\Omega$, $C_L = 35\text{pF}$, Test Circuit 2	+25°C		45		ns
Turn-Off Time	t_{OFF}		+25°C		35		ns
Break-Before-Make Time Delay	t_D	V_{NO} or $V_{NC} = 2.0\text{V}$, $R_L = 50\Omega$, $C_L = 35\text{pF}$, Test Circuit 3	+25°C		10		ns
Charge Injection	Q	$C_L = 1.0\text{nF}$, $V_G = 2.8\text{V}$, $R_G = 0\Omega$, Test Circuit 4	+25°C		80		pC
Off Isolation	O_{ISO}	Signal = 0dBm, $V_{BIAS} = 2.5\text{V}$, $R_L = 50\Omega$, Test Circuit 5	$f = 100\text{kHz}$	+25°C	-75		dB
			$f = 1\text{MHz}$	+25°C	-55		
Channel-to-Channel Crosstalk	X_{TALK}	Signal = 0dBm, $V_{BIAS} = 2.5\text{V}$, $R_L = 50\Omega$, Test Circuit 6	$f = 1\text{MHz}$	+25°C	-80		dB
			$f = 10\text{MHz}$	+25°C	-60		
-3dB Bandwidth	BW	Signal = 0dBm, $V_{BIAS} = 2.5\text{V}$, $R_L = 50\Omega$, Test Circuit 7	+25°C		40		MHz
Channel ON Capacitance	$C_{NC(ON)}$, $C_{NO(ON)}$, $C_{COM(ON)}$		+25°C		185		pF

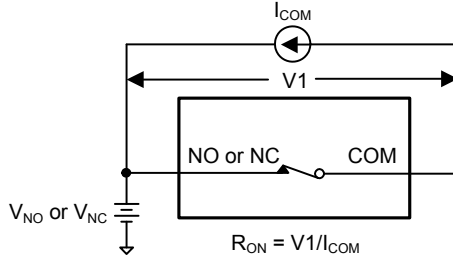
TYPICAL PERFORMANCE CHARACTERISTICS



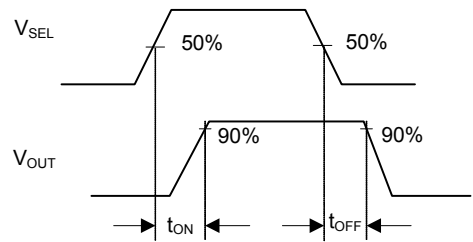
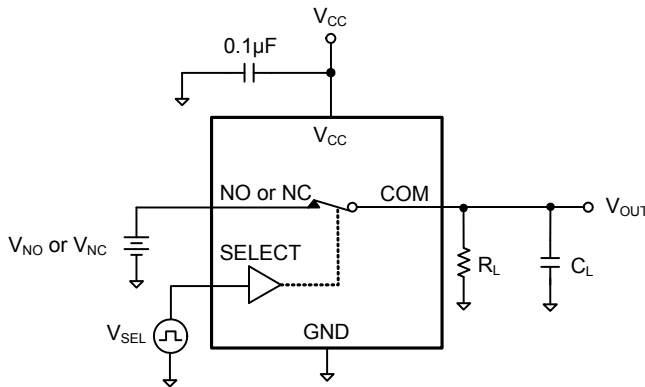
TYPICAL PERFORMANCE CHARACTERISTICS



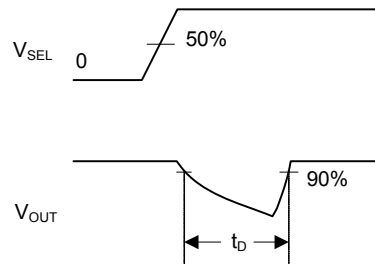
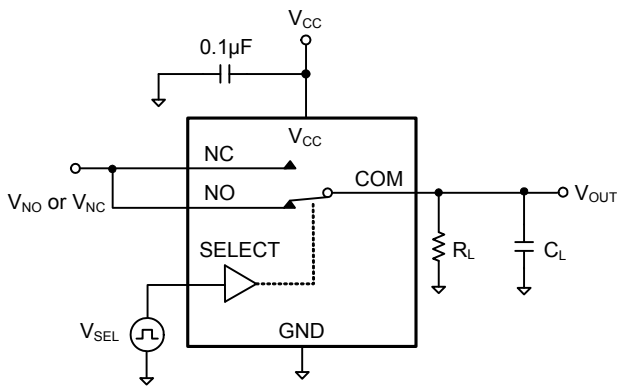
TEST CIRCUITS



Test Circuit 1. On Resistance

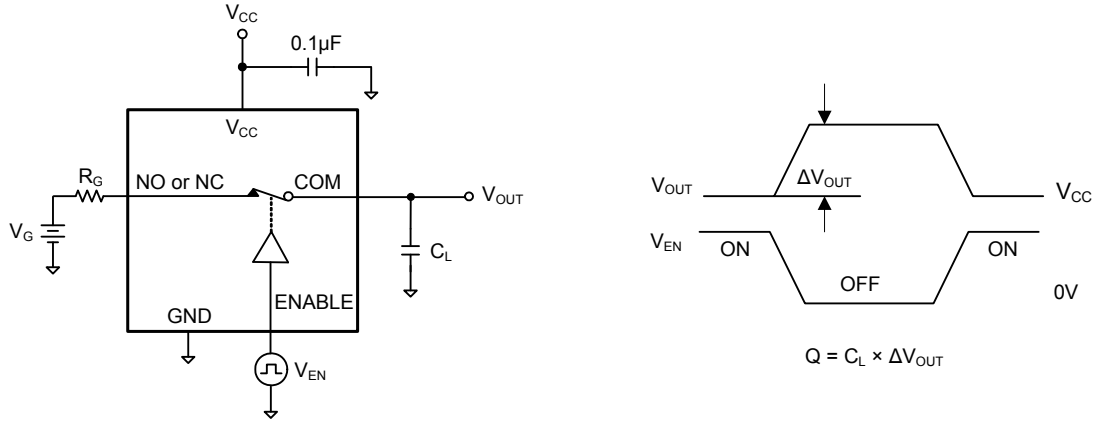


Test Circuit 2. Switching Times (t_{ON} , t_{OFF})

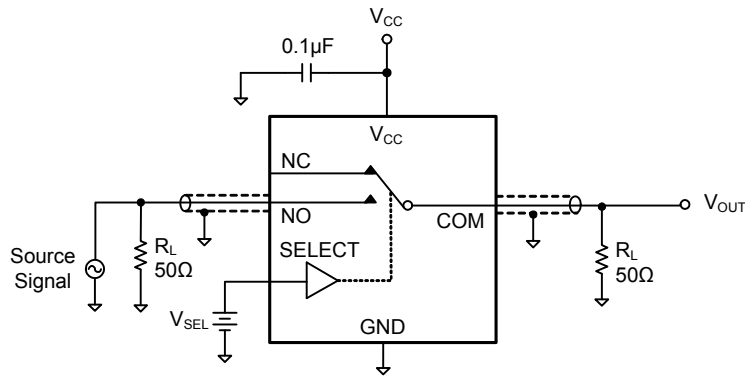


Test Circuit 3. Break-Before-Make Time Delay (t_D)

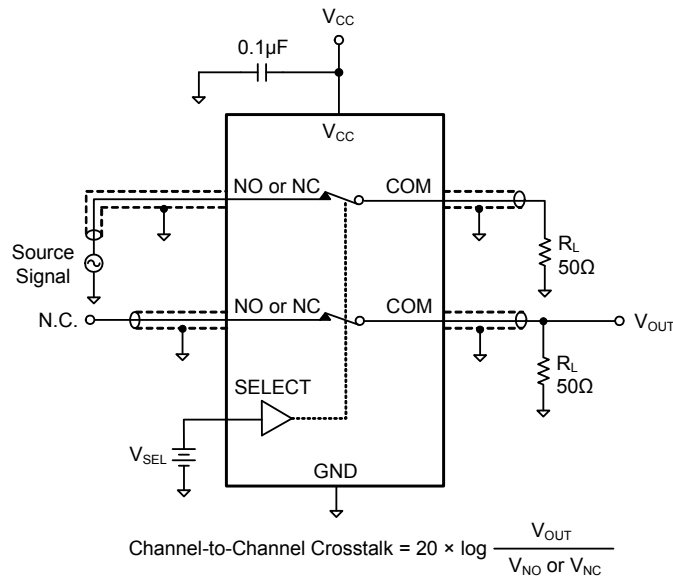
TEST CIRCUITS (Cont.)



Test Circuit 4. Charge Injection

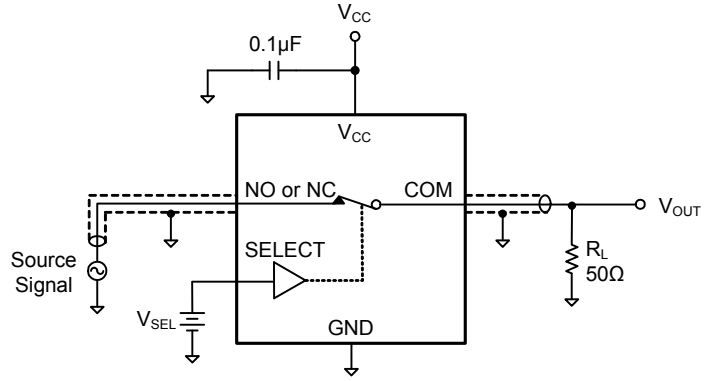


Test Circuit 5. Off Isolation



Test Circuit 6. Channel-to-Channel Crosstalk

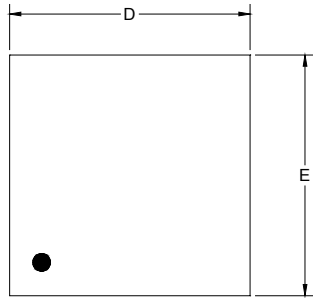
TEST CIRCUITS (Cont.)



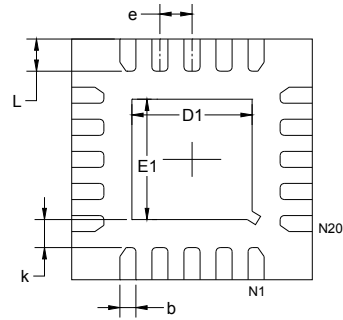
Test Circuit 7. -3dB Bandwidth

PACKAGE OUTLINE DIMENSIONS

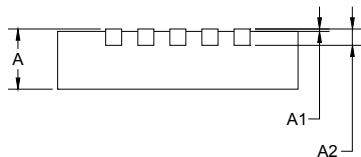
TQFN-3x3-20L



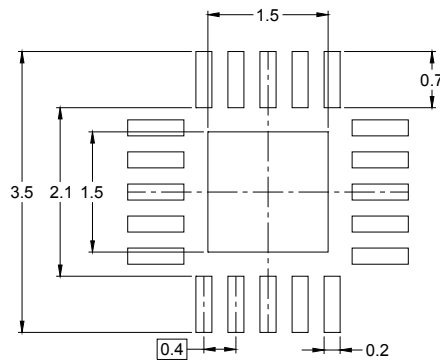
TOP VIEW



BOTTOM VIEW



SIDE VIEW

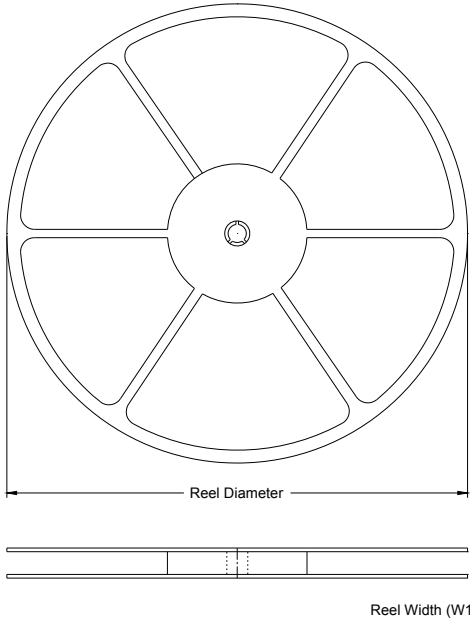


RECOMMENDED LAND PATTERN (Unit: mm)

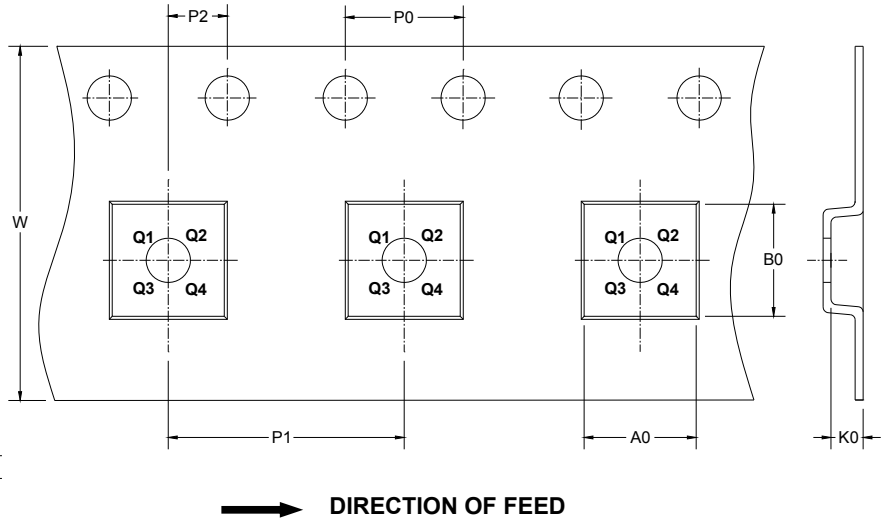
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A2	0.203 REF		0.008 REF	
D	2.924	3.076	0.115	0.121
D1	1.400	1.600	0.055	0.063
E	2.924	3.076	0.115	0.121
E1	1.400	1.600	0.055	0.063
k	0.200 MIN		0.008 MIN	
b	0.150	0.250	0.006	0.010
e	0.400 TYP		0.016 TYP	
L	0.324	0.476	0.013	0.019

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS

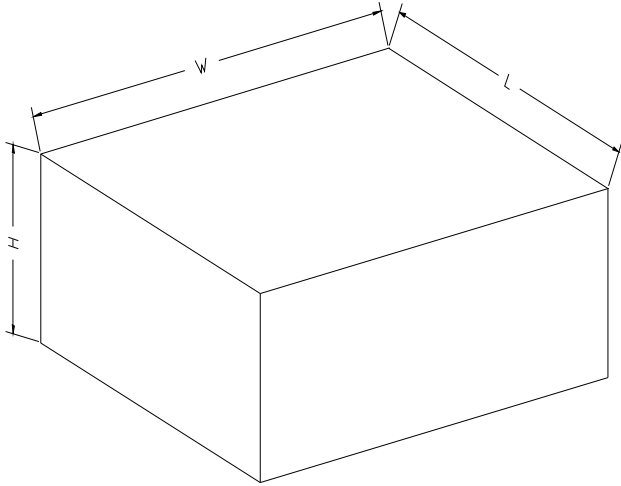


NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
TQFN-3x3-20L	13"	12.4	3.3	3.3	1.1	4.0	4.0	2.0	12.0	Q1

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
13"	386	280	370	5