

SGM66057 2.2MHz, Fixed Output Synchronous Tiny Boost Converter with a 3.4A Switch

GENERAL DESCRIPTION

The SGM66057 is an internally compensated, 2.2MHz switching frequency, current mode, synchronous Boost switching converter. The output voltage is fixed at 5.25V. The device turns into power-save mode to maintain high efficiency at light load currents. Its output is disconnected by the rectifier circuit during shutdown, with no input to output leakage.

Various protection features such as over-current, short-circuit and over-temperature are implemented to protect the device from various fault conditions.

The SGM66057 is available in a Green WLCSP-1.26×1.21-9B package.

FEATURES

- 2.7V to 4.85V Operating Input Voltage Range
- 5.25V Fixed Output Voltage
- Up to 96.8% Efficiency
- Device Quiescent Current: 24µA (TYP)
- Shutdown Current: 1µA (MAX)
- Valley Switch Current Limit: 3.4A (TYP)
- Improved Light Load Efficiency with Power-Save Mode (PSM)
- Low Reverse Leakage Current when V_{OUT} > V_{IN}
- Load Disconnect during Shutdown
- Over-Temperature Protection
- Available in a Green WLCSP-1.26×1.21-9B Package

APPLICATIONS

Class-D Audio Amplifier and USB OTG Supply Boost for Low-Voltage Li-Ion Batteries Smart Phones and Tablets Portable and Wearable Devices

代理商 威尔迪 W 联系电话 TYPICAL APPLICATION

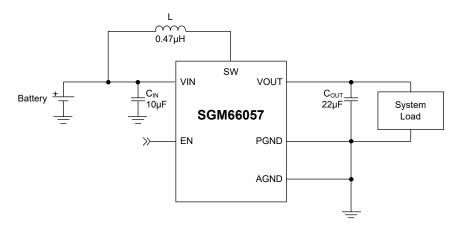


Figure 1. Typical Application Circuit

SGM66057

PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION	
SGM66057-5.25	WLCSP-1.26×1.21-9B	-40°C to +85°C	SGM66057-5.25YG/TR	XXXX 024	Tape and Reel, 3000	

MARKING INFORMATION

NOTE: XXXX = Date Code, Trace Code and Vendor Code.

ХХХХ	
TTL	- Vendor Code
	- Trace Code
	Date Code - V

ode Date Code - Year

Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

VIN Voltage	-0.3V to 5.5V
VOUT and EN Voltages	0.3V to 6V
SW Node (DC)	0.3V to 6V
SW Node (Transient: 10ns, 3MHz)	1V to 8V
Voltage on Other Pins	0.3V to 6V ⁽¹⁾
Package Thermal Resistance	
WLCSP-1.26×1.21-9Β, θ _{JA}	90°C/W
Junction Temperature	+150°C
Storage Temperature Range	
Lead Temperature (Soldering, 10s)	+260°C
ESD Susceptibility	EP W
НВМ	4000V
CDM	1000V

NOTE: 1. Lesser of 6V or V_{IN} + 0.3V.

RECOMMENDED OPERATING CONDITIONS

Operating Ambient Temperature Range -40°C to +85°C Operating Junction Temperature Range -40°C to +125°C

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

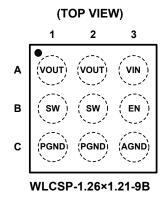
DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.



SGM66057

PIN CONFIGURATION



PIN DESCRIPTION

PIN	NAME	FUNCTION
A1, A2	VOUT	Boost Converter Output.
A3	VIN	Power Supply Input.
B1, B2	SW	Switching Node. Drain connection of low-side power MOSFET.
В3	EN	Device Enable Node. Pulling this pin logic high enables the device and pulling it logic low disables the device.
C1, C2	PGND	Power Ground.
C3	AGND	Analog Ground.
		BK 22.

ELECTRICAL CHARACTERISTICS

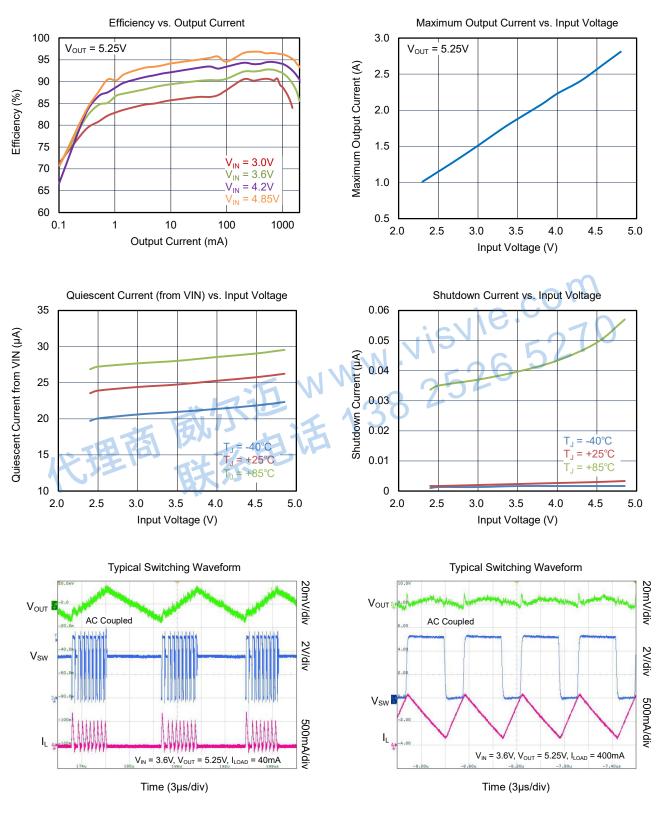
(V_{IN} = 3.6V, T_J = -40°C to +85°C, typical values are at T_J = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
DC/DC Stage		·			-	
Input Voltage	V _{IN}	$T_J = +25^{\circ}C$	2.7		4.85	V
UVLO Rising Threshold	V _{UVLO_R}	$T_J = +25^{\circ}C$	2.03	2.34	2.66	V
UVLO Falling Threshold	V_{UVLO_F}	$T_J = +25^{\circ}C$	1.97	2.27	2.58	V
UVLO Hysteresis Voltage	V _{UVLO_HYS}			70		mV
Switching Frequency	f		1.76	2.2	2.64	MHz
Switch Current Limit	IL.	$T_J = +25^{\circ}C$	3	3.4	4.1	А
Boost Switch On-Resistance	R _{DSON}	V _{OUT} = 5.0V, T _J = +25°C		55	85	mΩ
Rectifying Switch On-Resistance	R _{DSON}	V _{OUT} = 5.0V, T _J = +25°C		75	100	mΩ
Output Voltage	V _{OUT}		5.14	5.25	5.38	V
Quiescent Current	Ι _Q	$V_{EN} = V_{IN} = 3.6V$, no switching, $T_J = +25^{\circ}C$		24	37	μA
Shutdown Current	I _{SD}	V _{EN} = 0V, V _{IN} = 3.6V, T _J = +25°C		0.01	1	μA
Control Stage		•				
EN Input Low Voltage	VIL				0.4	V
EN Input High Voltage	V _{IH}		1.3	CO		V
EN Input Current	I _{EN}	Clamped to GND or VIN	110	0.1	10	μA
Over-Temperature Protection	T _{SD}			150		°C
Over-Temperature Hysteresis	T _{SD_HYS}		06	20		°C
代理商	威联	永电话 138 ²⁵	020			



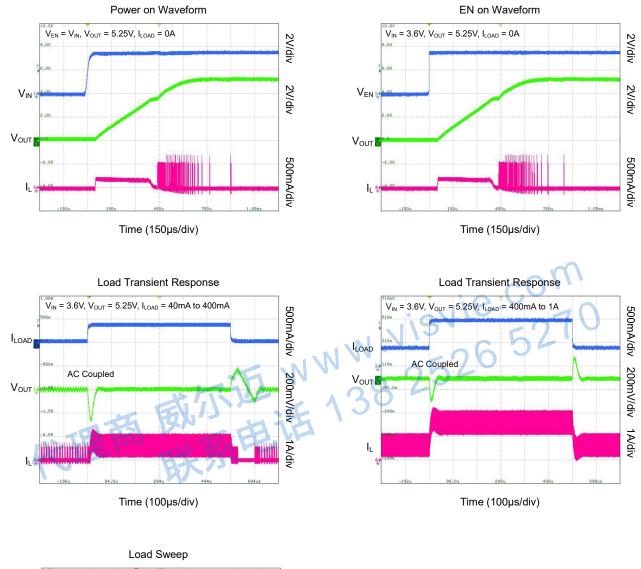
TYPICAL PERFORMANCE CHARACTERISTICS

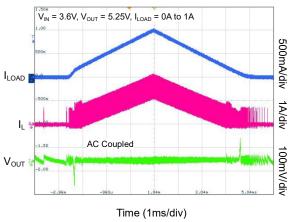
At T_J = +25°C, V_{IN} = 3.6V, C_{IN} = 10µF, C_{OUT} = 20µF and L = 0.47µH, unless otherwise noted.



TYPICAL PERFORMANCE CHARACTERISTICS (continued)

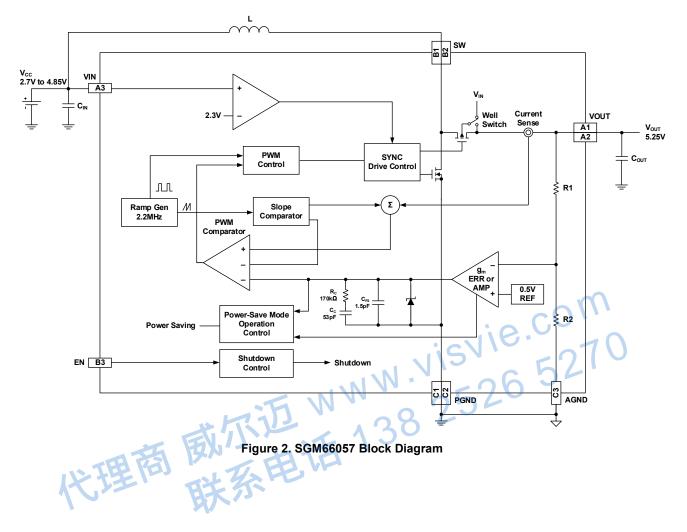
At T_J = +25°C, V_{IN} = 3.6V, C_{IN} = 10µF, C_{OUT} = 20µF and L = 0.47µH, unless otherwise noted.





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FUNCTIONAL BLOCK DIAGRAM



APPLICATION INFORMATION

The SGM66057 is a fully-integrated synchronous Boost DC/DC converter. The recommended input supply voltage for full performance is 2.7V to 4.85V. Operating input voltage above 4.85V is possible with SGM66057, however, the full performance is not guaranteed beyond the 4.85V operation. When the input supply voltage is larger than 5.3V, the device is shut down. An inductor, an output storage capacitor and an input decoupling capacitor should be selected to ensure the proper performance desired in a specific application circuit.

Start-up and Enable

The SGM66057 implements the enable input to control the turn-on and turn-off of the device. A logic signal above 1.3V applied on EN pin turns on the device, and a logic signal below 0.4V turns off the device.

The SGM66057 has built-in 500µs (TYP) soft-start time. After enabling, the SGM66057 enters linear pre-charge phase with a limited current of 200mA. As the output voltage reaches the input voltage, the pre-charge phase terminates. The SGM66057 starts switching and boosts the output voltage to the 5.25V fixed output. This start-up sequence effectively reduces the inrush current during start-up.

The SGM66057 has a built-in 0.1µA (TYP) pull-down current when the EN pin is programmed to logic low. In addition to this pull-down current, there is an additional $300k\Omega$ pull-down resistor when EN pin is logic low. When EN pin is programmed to logic high, the $300k\Omega$ pull-down resistor is switched off and only the 0.1µA (TYP) pull-down current remains.

Current Limit and Short-Circuit

The SGM66057 has a built-in 3.4A (TYP) current limit. When inductor current exceeds the valley current limit, output voltage will fall to maintain a constant power operation. As output voltage drops below the input voltage, current limit is reduced to 200mA (TYP) to minimize excessive power dissipation within the IC. When an output short to ground event occurs, the SGM66057 reduces the current limit to 200mA (TYP) to prevent damage to the device. The SGM66057 resumes operation and goes through the start-up sequence once the short-circuit condition is removed.

Inductor Selection

Inductor is an essential element for current DC/DC switch mode power supplies regardless of topology. Inductor serves as the energy storage element for power conversion. Inductance and saturation current of inductor are two most important criterions for inductor selection. For general design guidance, the selected inductance should provide a peak to peak ripple current that is around 30% of the average inductor current at full load and nominal input voltage. The average inductor current for a Boost converter is the input current. Equation 1 shows the calculation of inductance selection, where $f_{\rm SW}$ is the switching frequency and $\Delta I_{\rm L}$ is the inductor ripple current.

$$= \frac{V_{IN} \times (V_{OUT} - V_{IN})}{\Delta I_{L} \times f_{SW} \times V_{OUT}}$$
(1)

The 3.4A (TYP) valley current limit and the inductor current ripple should be considered when selecting the saturation current of the inductor.

The inductor also affects the close loop response of the DC/DC converter. The SGM66057 is an internally compensated device, and the loop response is optimized for inductor in the range of 0.33μ H to 1.3μ H.

Input Capacitor

Boost converter's input capacitor has continuous current throughout the entire switching cycle, a 10μ F ceramic capacitor is recommended to place as close as possible between the VIN pin and GND pin of the device. For applications where the SGM66057 is located far away from the input source, a 47μ F or higher capacitance capacitor is recommended to damp the wiring harness inductance.



SGM66057

APPLICATION INFORMATION (continued)

Output Capacitor

The output capacitors of Boost converter dictate the output voltage ripple and load transient response. Equation 2 is used to estimate the necessary capacitance to achieve desired output voltage ripple, where ΔV is the maximum allowed ripple.

$$C_{MIN} = \frac{I_{OUT} \times (V_{OUT} - V_{IN})}{f_{SW} \times \Delta V \times V_{OUT}}$$
(2)

Since SGM66057 is an internally compensated device, the loop response is optimized for capacitor in the range of 10µF to 47µF. Due to the DC bias nature of ceramic capacitors, care should be taken by verifying manufacturer's datasheet to ensure enough effective capacitance at desired output voltage.

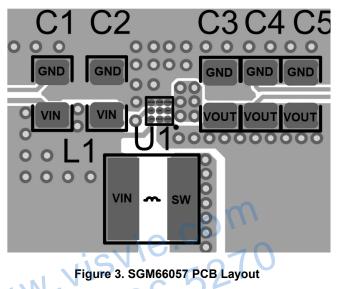
Layout Considerations

In addition to component selection, layout is a critical step to ensure the performance of any switch mode - Tor Figure 3. SGM6 Juctance. power supplies. Poor layout could result in system instability, EMI failure, and device damage. Thus, place the inductor, input and output capacitors as close to the IC as possible, and use wide and short traces for current carrying traces to minimize PCB inductance.

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For Boost converter, the current loop of the output capacitor from VOUT pin back to the PGND pin of the device should be as small as possible.

Layout Example



REVISION HISTORY

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

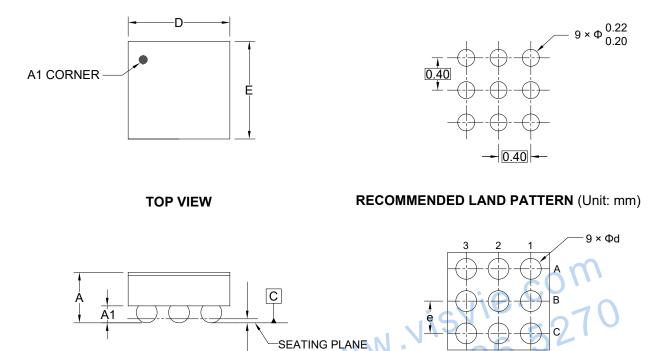
Changes from Original (MAY 2023) to REV.A

Changes from Original (MAY 2023) to REV.A	Page
Changed from product preview to production data	All

PACKAGE OUTLINE DIMENSIONS

SIDE VIEW

WLCSP-1.26×1.21-9B



Symbol	Dimensions In Millimeters						
Symbol	MIN	MOD	MAX				
A	0.587	0.625	0.663				
A1	0.190	0.210	0.230				
D	1.230	1.260	1.290				
E	1.180	1.210	1.240				
d	0.228	0.258	0.288				
е		0.400 BSC					
ccc	0.050						

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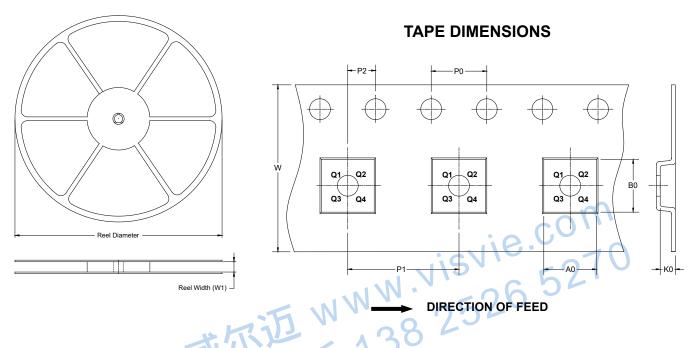
BOTTOM VIEW

NOTE: This drawing is subject to change without notice.



TAPE AND REEL INFORMATION

REEL DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

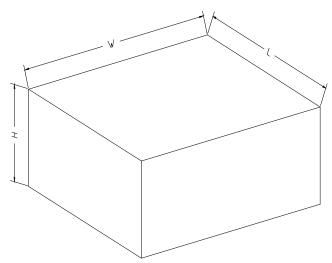
KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
WLCSP-1.26×1.21-9B	7"	9.2	1.40	1.43	0.69	4.0	4.0	2.0	8.0	Q2



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CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

Reel Type	R LIST OF	Width (mm)	Height (mm)	Pizza/Carton	vie. 527
7" (Option)	368	227	224	082	520
7"	442	410	224	3 18	DD0002

