TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA78L05F,TA78L06F,TA78L07F,TA78L08F,TA78L09F,TA78L10F,TA78L12F,TA78L15F,TA78L18F,TA78L20F,TA78L24F

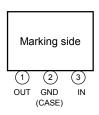
5 V, 6 V, 7 V, 8 V, 9 V, 10 V, 12 V, 15 V, 18 V, 20 V, 24 V

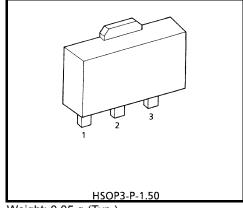
3-Terminal Positive Voltage Regulators

Features

- Best suited to power supply for TTL/CMOS.
- No external parts needed.
- Built-in overheating protection.
- Built-in overcurrent protection.
- Max output current of 150mA. (Tj = 25°C).
- Packaged in PW-mini (SOT-89).

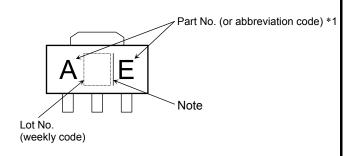
Pin Assignment





Weight: 0.05 g (Typ.)

Marking



	Part No. (or abbreviation code)	Part No.
	AE	TA78L05F
	BE	TA78L06F
	KE	TA78L07F
	CE	TA78L08F
*1	DE	TA78L09F
	EE	TA78L10F
	FE	TA78L12F
	GE	TA78L15F
	HE	TA78L18F
	ΙΕ	TA78L20F
	JE	TA78L24F

Note: A line beside a Lot No. identifies the indication of product Labels.

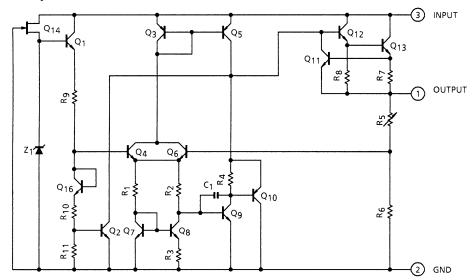
Without a line: [[Pb]]/INCLUDES > MCV

With a line: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

The product(s) in this document ("Product") contain functions intended to protect the Product from temporary small overloads such as minor short-term overcurrent or overheating. The protective functions do not necessarily protect Product under all circumstances. When incorporating Product into your system, please design the system (1) to avoid such overloads upon the Product, and (2) to shut down or otherwise relieve the Product of such overload conditions immediately upon occurrence. For details, please refer to the notes appearing below in this document and other documents referenced in this document.

Equivalent Circuit



Туре	Marking
TA78L05F	AE
TA78L06F	BE
TA78L07F	KE
TA78L08F	CE
TA78L09F	DE
TA78L10F	EE
TA78L12F	FE
TA78L15F	GE
TA78L18F	HE
TA78L20F	IE
TA78L24F	JE

Absolute Maximum Ratings (Ta = 25°C)

Characteris	tics	Symbol	Rating	Unit
	TA78L05F			
	TA78L06F			
	TA78L07F			
	TA78L08F		35	
	TA78L09F		33	
Olnput voltage	TA78L10F	V_{IN}		V
	TA78L12F			
	TA78L15F			
	TA78L18F			
	TA78L20F		40	
	TA78L24F			
Output current		lout	0.15	Α
Power dissipation	(Ta = 25°C)	P_{D}	500	mW
Operating temperature Storage temperature		T _{opr}	−30 to 85	°C
		T _{stg}	-55 to 150	°C
Junction temperature		Tj	150	°C
Thermal resistance		R _{th (j-a)}	250	°C/W

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

TA78L05F Electrical Characteristics (Unless otherwise specified, V_{IN} = 10 V, I_{OUT} = 40 mA, C_{IN} = 0.33 $\mu F,$ C_{OUT} = 0.1 $\mu F,$ 0°C \leq T_{j} \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		4.75	5.0	5.25	V
Line regulation	Reg·line	1	T _i = 25°C	7.0 V ≤ V _{IN} ≤ 20 V	_	55	150	mV
Line regulation	Regilile	'	1j - 25 C	8.0 V ≤ V _{IN} ≤ 20 V	_	45	100	IIIV
Load regulation	Poguland	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	_	11	60	mV
Load regulation	Reg·load	'	1j - 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	5.0	30	IIIV
Output voltage	Vout	1	T _j = 25°C	7.0 V ≤ V _{IN} ≤ 20 V, 1.0 mA ≤ I _{OUT} ≤ 40 mA	4.65	_	5.35	٧
				1.0 mA ≤ I _{OUT} ≤ 70 mA	4.65	_	5.35	
Quiescent current	1-	1	T _j = 25°C		_	3.1	6.0	mA
Quiescent current	I _B	'	T _j = 125°C	Т _ј = 125°С		_	5.5	IIIA
Quiescent current change	۸۱-	1	T _i = 25°C	8.0 V ≤ V _{IN} ≤ 20 V	_	_	1.5	mA
Quiescent current change	Δl _B	'	1j - 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	_	0.1	IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz	_	40	_	μV_{rms}
Long term stability	ΔV _{OUT} /Δt	1		_	_	12	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz, 8.0 V ≤ V _{IN} ≤ 18 V, T _j = 25°C		41	49	_	dB
Dropout voltage	V _D	1	T _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 n	nA	_	-0.6	_	mV/°C

TA78L06F Electrical Characteristics (Unless otherwise specified, V_{IN} = 11 V, I_{OUT} = 40 mA, C_{IN} = 0.33 $\mu F,$ C_{OUT} = 0.1 $\mu F,$ 0°C \leq T_{j} \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		5.7	6.0	6.3	V
Line regulation	Reg·line	1	T _i = 25°C	8.1 V ≤ V _{IN} ≤ 21 V	_	50	150	mV
Line regulation	Regime	'	1j - 25 C	9.0 V ≤ V _{IN} ≤ 21 V	_	45	110	IIIV
Load regulation	Reg·load	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	_	12	70	mV
Load regulation	Regiload	'	1j - 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	5.5	35	IIIV
Output voltage	Vout	1	T _i = 25°C	8.1 V ≤ V _{IN} ≤ 21 V, 1.0 mA ≤ I _{OUT} ≤ 40 mA	5.58	_	6.42	V
			I ' -	1.0 mA ≤ I _{OUT} ≤ 70 mA	5.58	_	6.42	
Quiescent current	1_	1	T _j = 25°C		_	3.1	6.0	mA
Quiescent current	I _B	'	T _j = 125°C	Γ _j = 125°C		_	5.5	IIIA
Quissaant aurrant ahanga	Δ1-	1	T _i = 25°C	9.0 V ≤ V _{IN} ≤ 20 V	_	_	1.5	mA
Quiescent current change	Δl _B	'	1j = 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	_	0.1	IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz	_	40	_	μV_{rms}
Long term stability	ΔV _{OUT} /Δt	1		_	_	14	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz, 9.0 V ≤ V _{IN} ≤ 19 V, T _j = 25°C		39	47	_	dB
Dropout voltage	V _D	1	T _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 n	nA	_	-0.7	_	mV/°C

TA78L07F Electrical Characteristics (Unless otherwise specified, V_{IN} = 12 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μ F, C_{OUT} = 0.1 μ F, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		6.65	7.0	7.35	V
Line regulation	Regiline	1	T _i = 25°C	9.2 V ≤ V _{IN} ≤ 22 V	_	50	160	mV
Line regulation	Reguine	'	1j - 25 C	10 V ≤ V _{IN} ≤ 22 V	_	45	115	IIIV
Load regulation	Reg·load	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	_	13	75	mV
Load regulation	Regiload	'	1j - 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	6.0	40	IIIV
Output voltage	Vout	1	T _i = 25°C	9.2 V ≤ V _{IN} ≤ 22 V, 1.0 mA ≤ I _{OUT} ≤ 40 mA	6.51	_	7.49	V
			I ' F	1.0 mA ≤ I _{OUT} ≤ 70 mA	6.51	_	7.49	
Quiescent current	1_	1	T _j = 25°C		_	3.1	6.5	mA
Quiescent current	I _B	'	T _j = 125°C	-j = 125°C		_	6.0	IIIA
Quissaant aurrant ahanga	Δ1-	1	T 05°0	10 V ≤ V _{IN} ≤ 22 V	_	_	1.5	- mA
Quiescent current change	Δl _B	'	T _j = 25°C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	_	0.1	IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz	_	50	_	μV_{rms}
Long term stability	ΔV _{OUT} /Δt	1		_	_	17	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz, 10 V ≤ V _{IN} ≤ 20 V, T _j = 25°C		37	46	_	dB
Dropout voltage	V_{D}	1	T _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 n	nA	_	-0.75		mV/°C

TA78L08F Electrical Characteristics (Unless otherwise specified, V_{IN} = 14 V, I_{OUT} = 40 mA, C_{IN} = 0.33 $\mu F,$ C_{OUT} = 0.1 $\mu F,$ 0°C \leq T_{j} \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		7.6	8.0	8.4	V
Line regulation	Regiline	1	T _i = 25°C	10.5 V ≤ V _{IN} ≤ 23 V	_	20	175	mV
Line regulation	Reguine	'	1j - 25 C	11 V ≤ V _{IN} ≤ 23 V	_	12	125	IIIV
Load regulation	Reg·load	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	_	15	80	mV
Load regulation	Regiload	'	1j - 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	7.0	40	IIIV
Output voltage	Vout	1	T _i = 25°C	10.5 V ≤ V _{IN} ≤ 23 V, 1.0 mA ≤ I _{OUT} ≤ 40 mA	7.44	_	8.56	V
			1 ' -	1.0 mA ≤ I _{OUT} ≤ 70 mA	7.44	_	8.56	
Quiescent current	1-	1	T _j = 25°C		_	3.1	6.5	mA
Quiescent current	I _B	'	T _j = 125°C	Γ _j = 125°C		_	6.0	IIIA
Quissaant aurrant ahanga	Δ1-	1	T 05°0	11 V ≤ V _{IN} ≤ 23 V	_	_	1.5	mA
Quiescent current change	Δl _B	'	T _j = 25°C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	_	0.1	IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz	_	60	_	μV_{rms}
Long term stability	ΔV _{OUT} /Δt	1		_	_	20	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz, 12 V ≤ V _{IN} ≤ 23 V, T _j = 25°C		37	45	_	dB
Dropout voltage	V_{D}	1	T _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 n	nA	_	-0.8	_	mV/°C

TA78L09F Electrical Characteristics (Unless otherwise specified, V_{IN} = 15 V, I_{OUT} = 40 mA, C_{IN} = 0.33 $\mu F,$ C_{OUT} = 0.1 $\mu F,$ 0°C \leq T_{j} \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		8.55	9.0	9.45	V
Line regulation	Regiline	1	T _i = 25°C	11.4 V ≤ V _{IN} ≤ 24 V	_	80	200	mV
Line regulation	Reguine	'	1j - 25 C	12 V ≤ V _{IN} ≤ 24 V	_	20	160	IIIV
Load regulation	Reg·load	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	_	17	90	mV
Load regulation	Regiload	'	1j - 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	8.0	45	IIIV
Output voltage	Vout	1	T _i = 25°C	11.4 V ≤ V _{IN} ≤ 24 V, 1.0 mA ≤ I _{OUT} ≤ 40 mA	8.37	_	9.63	٧
			I ' -	1.0 mA ≤ I _{OUT} ≤ 70 mA	8.37	_	9.63	
Quiescent current	1_	1	T _j = 25°C		_	3.2	6.5	mA
Quiescent current	I _B	'	T _j = 125°C	T _j = 125°C		_	6.0	IIIA
Quissaant aurrant ahanga	Δ1-	1	T _i = 25°C	12 V ≤ V _{IN} ≤ 24 V	_	_	1.5	- mA
Quiescent current change	Δl _B	'	1j - 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	_	0.1	IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz	_	65	_	μV_{rms}
Long term stability	ΔV _{OUT} /Δt	1		_	_	21	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz, 12 V ≤ V _{IN} ≤ 24 V, T _j = 25°C		36	44	_	dB
Dropout voltage	V_{D}	1	T _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 n	nA	_	-0.85		mV/°C

TA78L10F Electrical Characteristics (Unless otherwise specified, V_{IN} = 16 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μF, C_{OUT} = 0.1 μF, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		9.5	10	10.5	V
Line regulation	Reg·line	1	T _i = 25°C	12.5 V ≤ V _{IN} ≤ 25 V	_	80	230	mV
Line regulation	Reguine	'	1j - 25 C	13 V ≤ V _{IN} ≤ 25 V	_	30	170	IIIV
Load regulation	Reg·load	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	_	18	90	mV
Load regulation	Regiload	'	1j - 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	8.5	45	IIIV
Output voltage	Vout	1	T _j = 25°C	12.5 V ≤ V _{IN} ≤ 25 V, 1.0 mA ≤ I _{OUT} ≤ 40 mA	9.3	_	10.7	V
				1.0 mA ≤ I _{OUT} ≤ 70 mA	9.3	_	10.7	
Quiescent current	1_	1	T _j = 25°C		_	3.2	6.5	mA
Quiescent current	I _B	'	T _j = 125°C	Γ _j = 125°C		_	6.0	IIIA
Quissaant aurrant ahanga	Δ1-	1	T 05°0	13 V ≤ V _{IN} ≤ 25 V	_	_	1.5	mA
Quiescent current change	Δl _B	'	T _j = 25°C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	_	0.1	IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz	_	70	_	μV_{rms}
Long term stability	ΔV _{OUT} /Δt	1		_	_	22	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz, 13 V ≤ V _{IN} ≤ 24 V, T _j = 25°C		36	43	_	dB
Dropout voltage	V_{D}	1	T _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 n	nA	_	-0.9	_	mV/°C

TA78L12F Electrical Characteristics (Unless otherwise specified, V_{IN} = 19 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μ F, C_{OUT} = 0.1 μ F, 0°C \leq T $_j$ \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		11.4	12	12.6	V
Line regulation	Regiline	1	T _i = 25°C	14.5 V ≤ V _{IN} ≤ 27 V	_	120	250	mV
Line regulation	Regime	'	1j - 25 C	16 V ≤ V _{IN} ≤ 27 V	_	100	200	IIIV
Load regulation	Reg·load	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	_	20	100	mV
Load regulation	Regiload	'	1j - 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	10	50	IIIV
Output voltage	Vout	1	T _i = 25°C	14.5 V ≤ V _{IN} ≤ 27 V, 1.0 mA ≤ I _{OUT} ≤ 40 mA	11.16	_	12.84	٧
			I ' F	1.0 mA ≤ I _{OUT} ≤ 70 mA	11.16	_	12.84	
Quiescent current	1_	1	T _j = 25°C		_	3.2	6.5	mA
Quiescent current	I _B	'	T _j = 125°C	T _j = 125°C		_	6.0	IIIA
Quiceant aurrent change	Δ1-	1	T _i = 25°C	16 V ≤ V _{IN} ≤ 27 V	_	_	1.5	mA
Quiescent current change	Δl _B	'	1j - 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	_	0.1	IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz	_	80	_	μV_{rms}
Long term stability	ΔV _{OUT} /Δt	1		_	_	24	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz, 15 V ≤ V _{IN} ≤ 25 V, T _j = 25°C		36	41	_	dB
Dropout voltage	V _D	1	T _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 n	nA	_	-1.0	_	mV/°C

TA78L15F Electrical Characteristics (Unless otherwise specified, V_{IN} = 23 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μ F, C_{OUT} = 0.1 μ F, 0°C \leq T $_{j}$ \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		14.25	15	15.75	V
Line regulation	Reg·line	1	T _i = 25°C	17.5 V ≤ V _{IN} ≤ 30 V	_	130	300	mV
Line regulation	Regilile	'	1j - 25 C	20 V ≤ V _{IN} ≤ 30 V	_	110	250	IIIV
Load regulation	Poguland	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	_	25	150	mV
Load regulation	Reg·load	'	1j - 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	12	75	IIIV
Output voltage	Vout	1	T _j = 25°C	17.5 V ≤ V _{IN} ≤ 30 V, 1.0 mA ≤ I _{OUT} ≤ 40 mA	13.95	_	16.05	V
				1.0 mA ≤ I _{OUT} ≤ 70 mA	13.95	_	16.05	
Quiescent current	1-	1	T _j = 25°C		_	3.3	6.5	mA.
Quiescent current	I _B	'	T _j = 125°C	T _j = 125°C		_	6.0	IIIA
Quiescent current change	$\Delta I_{ m B}$	1	T _i = 25°C	20 V ≤ V _{IN} ≤ 30 V	_	_	1.5	mA
Quiescent current change	ΔIB	'	1j - 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	_	0.1	IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz	_	90	_	μV_{rms}
Long term stability	ΔV _{OUT} /Δt	1		_	_	30	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz, 18.5 V ≤ V _{IN} ≤ 28.5 V, T _j = 25°C		34	40	_	dB
Dropout voltage	V_{D}	1	T _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 n	nA		-1.3	_	mV/°C

TA78L18F Electrical Characteristics (Unless otherwise specified, V_{IN} = 27 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μF, C_{OUT} = 0.1 μF, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		17.1	18	18.9	V
Line regulation	Reg·line	1	T _i = 25°C	21.4 V ≤ V _{IN} ≤ 33 V	_	32	325	mV
Line regulation	Regime	'	1j - 25 C	22 V ≤ V _{IN} ≤ 33 V	_	27	275	IIIV
Load regulation	Pogulood	1	T _i = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	_	30	170	mV
Load regulation	Reg·load	'	1j - 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	15	75	IIIV
Output voltage	Vout	1	T _j = 25°C	21.4 V ≤ V _{IN} ≤ 33 V, 1.0 mA ≤ I _{OUT} ≤ 40 mA	16.74	_	19.26	V
				1.0 mA ≤ I _{OUT} ≤ 70 mA	16.74	_	19.26	
Quiescent current	1-	1	T _j = 25°C		_	3.3	6.5	mA.
Quiescent current	I _B	'	T _j = 125°C	T _j = 125°C		_	6.0	IIIA
Quiescent current change	۸۱-	1	T _i = 25°C	22 V ≤ V _{IN} ≤ 33 V	_	_	1.5	mA
Quiescent current change	Δl _B	'	1j - 25 C	1.0 mA ≤ I _{OUT} ≤ 40 mA	_	_	0.1	IIIA
Output noise voltage	V _{NO}	2	Ta = 25°C	, 10 Hz ≤ f ≤ 100 kHz	_	150	_	μV_{rms}
Long term stability	ΔV _{OUT} /Δt	1		_	_	45	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz, 23 V ≤ V _{IN} ≤ 33 V, T _j = 25°C		32	38	_	dB
Dropout voltage	V _D	1	T _j = 25°C	· · · · · · · · · · · · · · · · · · ·		1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 r	nA		-1.5	_	mV/°C

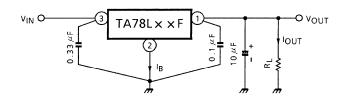
TA78L20F Electrical Characteristics (Unless otherwise specified, V_{IN} = 29 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μF, C_{OUT} = 0.1 μF, 0°C \leq T_j \leq 125°C)

Characteristics	Symbol	Test Circuit	Test Condition		Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		19.0	20	21.0	V
Line regulation	Reg·line	1	T _j = 25°C	23.5 V ≤ V _{IN} ≤ 35 V	_	33	330	mV
				24 V ≤ V _{IN} ≤ 35 V	_	28	285	
Load regulation	Reg·load	1	T _j = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	_	33	180	- mV
				1.0 mA ≤ I _{OUT} ≤ 40 mA	_	17	90	
Output voltage	V _{OUT}	1	T _j = 25°C	23.5 V ≤ V _{IN} ≤ 35 V, 1.0 mA ≤ I _{OUT} ≤ 40 mA	18.6	_	21.4	V
				1.0 mA ≤ I _{OUT} ≤ 70 mA	18.6	_	21.4	
Quiescent current	IB	1	T _j = 25°C		_	3.3	6.5	- mA
			T _j = 125°C		_	_	6.0	
Quiescent current change	Δl _B	1	T _j = 25°C	24 V ≤ V _{IN} ≤ 35 V	_	_	1.5	- mA
				1.0 mA ≤ I _{OUT} ≤ 40 mA	_	_	0.1	
Output noise voltage	V _{NO}	2	Ta = 25°C, 10 Hz ≤ f ≤ 100 kHz		_	170	_	μV_{rms}
Long term stability	ΔV _{OUT} /Δt	1	_		_	49	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz, 25 V \leq V _{IN} \leq 35 V, T _j = 25°C		31	37	_	dB
Dropout voltage	V_{D}	1	T _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 mA		_	-1.7	_	mV/°C

TA78L24F Electrical Characteristics (Unless otherwise specified, V_{IN} = 33 V, I_{OUT} = 40 mA, C_{IN} = 0.33 μ F, C_{OUT} = 0.1 μ F, 0° C \leq T_{j} \leq 125 $^{\circ}$ C)

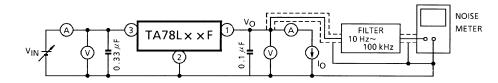
Characteristics	Symbol	Test Circuit	Test Condition		Min	Тур.	Max	Unit
Output voltage	V _{OUT}	1	T _j = 25°C		22.8	24	25.2	V
Line regulation	Reg·line	1	T _j = 25°C	27.5 V ≤ V _{IN} ≤ 38 V	_	35	350	mV
				28 V ≤ V _{IN} ≤ 38 V	_	30	300	
Load regulation	Reg·load	1	T _j = 25°C	1.0 mA ≤ I _{OUT} ≤ 100 mA	_	40	200	- mV
				1.0 mA ≤ I _{OUT} ≤ 40 mA	_	20	100	
Output voltage	V _{OUT}	1	T _j = 25°C	27.5 V ≤ V _{IN} ≤ 38 V, 1.0 mA ≤ I _{OUT} ≤ 40 mA	22.32	_	25.68	V
				1.0 mA ≤ I _{OUT} ≤ 70 mA	22.32	_	25.68	
Quiescent current	IB	1	T _j = 25°C		_	3.5	6.5	mA
			T _j = 125°C		_	_	6.0	
Quiescent current change	Δl _B	1	T _j = 25°C	28 V ≤ V _{IN} ≤ 38 V	_	_	1.5	- mA
				1.0 mA ≤ I _{OUT} ≤ 40 mA	_	_	0.1	
Output noise voltage	V _{NO}	2	Ta = 25°C, 10 Hz ≤ f ≤ 100 kHz		_	200	_	μV_{rms}
Long term stability	ΔV _{OUT} /Δt	1	_		_	56	_	mV/kh
Ripple rejection ratio	R.R.	3	f = 120 Hz, 29 V ≤ V _{IN} ≤ 39 V, T _j = 25°C		31	35	_	dB
Dropout voltage	V _D	1	T _j = 25°C		_	1.7	_	V
Average temperature coefficient of output voltage	T _{CVO}	1	I _{OUT} = 5 mA		_	-2.0	_	mV/°C

Test Circuit 1 / Standard Application



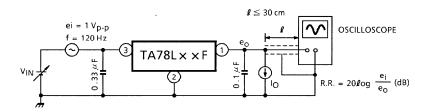
Test Circuit 2

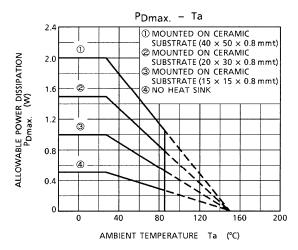
 V_{NO}

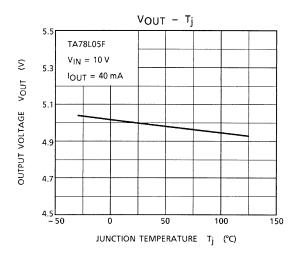


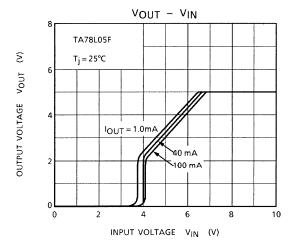
Test Circuit 3

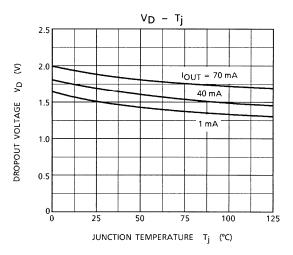
R.R.

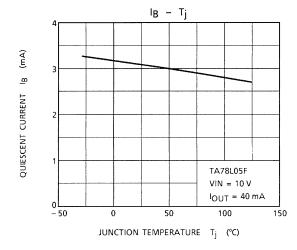


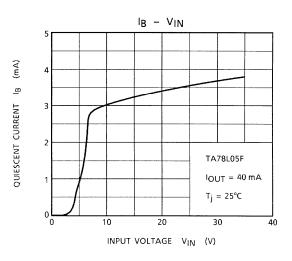


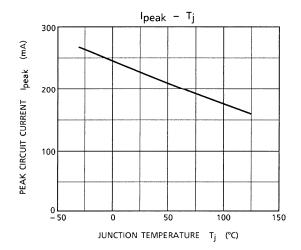


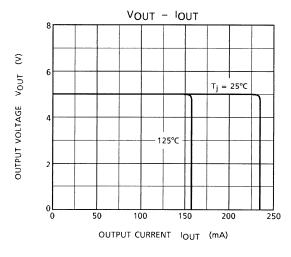






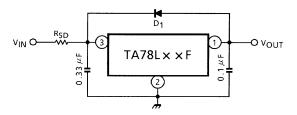






Usage Precautions

Destruction of the IC may occur if high voltage in excess of the IC output voltage (typ. value) is applied to the IC output terminal. Where this possibility exists, connect a Zener diode between the output terminal and GND to prevent any application of excessive voltage.



D₁ : IC protective diode

When surge voltage is applied to IC output terminal or $V_{\mbox{IN}} < V_{\mbox{OUT}}$ at the time of power ON/OFF, always connect the high speed switching diode D₁.

R_{SD}: Power limiting resistor

If $V_{\mbox{\scriptsize IN}}$ is too high, always connect RSD in order to reduce power consumption of IC.

• Low voltage

Do not apply voltage to the Product that is lower than the minimum operating voltage, or the Product's protective functions will not operate properly and the Product may be permanently damaged.

• Overcurrent Protection

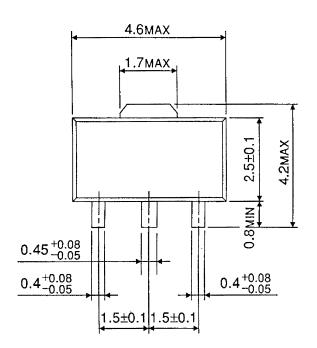
The overcurrent protection circuits in the Product are designed to temporarily protect Product from minor overcurrent of brief duration. When the overcurrent protective function in the Product activates, immediately cease application of overcurrent to Product. Improper usage of Product, such as application of current to Product exceeding the absolute maximum ratings, could cause the overcurrent protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

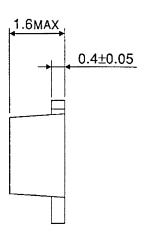
• Overheating Protection

The thermal shutdown circuits in the Product are designed to temporarily protect Product from minor overheating of brief duration. When the overheating protective function in the Product activates, immediately correct the overheating situation. Improper usage of Product, such as the application of heat to Product exceeding the absolute maximum ratings, could cause the overheating protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

Package Dimensions

HSOP3-P-1.50





Unit: mm



Weight: 0.05 g (Typ.)

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