RClamp0521Z
Ultra Small RClamp®
1-Line ESD protection

PROTECTION PRODUCTS - Z-Pak™

Description

RailClamp® TVS arrays are ultra low capacitance ESD protection devices designed to protect high speed data interfaces. They are designed to replace 0201 size multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and other portable electronics. This device offers desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

RClamp®0521Z has a typical capacitance of only 0.35pF. This allows it to be used on circuits operating in excess of 5GHz without appreciable signal attenuation.

RClamp0521Z is in a 2-pin SLP0603P2X3 package. It measures 0.6 x 0.3 mm with a nominal height of only 0.25mm. Leads are finished with lead-free NiAu. Each device will protect one line operating at 5 volts. It gives the designer the flexibility to protect single lines in applications where arrays are not practical. The combination of small size and high ESD surge capability makes them ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

Features

- High ESD withstand Voltage: **+/-17kV** (Contact) and **+/- 25kV** (Air) per **IEC 61000-4-2**
- Able to withstand over 1000 ESD strikes per IEC 61000-4-2 Level 4
- Ultra-small **0201 package**
- Protects one high-speed data line
- Low reverse current: <10nA typical (VR=5V)
- Working voltage: +/- 5V
- Low capacitance: 0.35pF typical
- Dynamic resistance: 0.90 Ohms (Typ)
- Solid-state silicon-avalanche technology

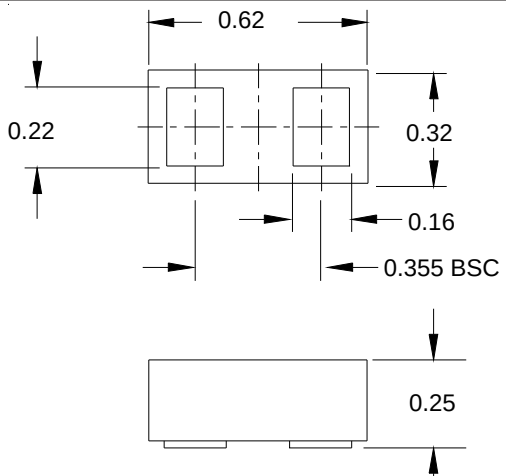
Mechanical Characteristics

- SLP0603P2X3 package
- Pb-Free, Halogen Free, RoHS/WEEE Compliant
- Nominal Dimensions: 0.6 x 0.3 x 0.25 mm
- Lead Finish: NiAu
- Molding compound flammability rating: UL 94V-0
- Marking : Marking code + dot matrix date code
- Packaging : Tape and Reel

Applications

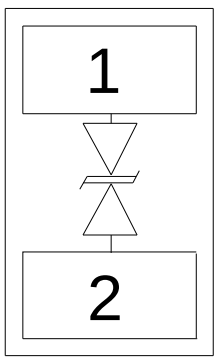
- HDMI 1.3 and HDMI 1.4
- USB 2.0
- MHL
- LVDS Interfaces
- FM Antenna
- PCI Express
- eSATA Interfaces

Dimensions



Nominal Dimensions (mm)

Circuit Diagram



SLP0603P2X3 (Bottom View)

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Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (tp = 8/20μs)	Ppk	100	Watts
Peak Pulse Current (tp = 8/20μs)	IPP	4	A
ESD per IEC 61000-4-2 (Air)[1] ESD per IEC 61000-4-2 (Contact)[1]	V_{ESD}	+/- 25 +/- 17	kV
Operating Temperature	T_J	-55 to +125	°C
Storage Temperature	T_{STG}	-55 to +150	°C

Electrical Characteristics (T=25°C)

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V_{RWM}				5	V
Reverse Breakdown Voltage	V_{BR}	I_t = 1mA	6	9.3	11	V
Reverse Leakage Current	I_R	V_{RWM} = 5V, T=25°C		0.005	0.100	μA
Clamping Voltage	V_C	I_{PP} = 1A, tp = 8/20μs			15	V
Clamping Voltage	V_C	I_{PP} = 4A, tp = 8/20μs			25	V
Dynamic Resistance[2, 3, 4]	R_D	tp = 100ns		0.90		Ohms
Junction Capacitance	C_j	V_R = 0V, f = 1MHz		0.35	0.50	pF

Notes
1)ESD gun return path connected to ESD ground reference plane.
2)Transmission Line Pulse Test (TLP) Settings: t_p = 100ns, t_r = 0.2ns, I_{TLP} and V_{TLP} averaging window: t_1 = 70ns to t_2 = 90ns.
3) Dynamic resistance calculated from I_{TLP} = 4A to I_{TLP} = 16A
4)Guaranteed by design. Not production tested

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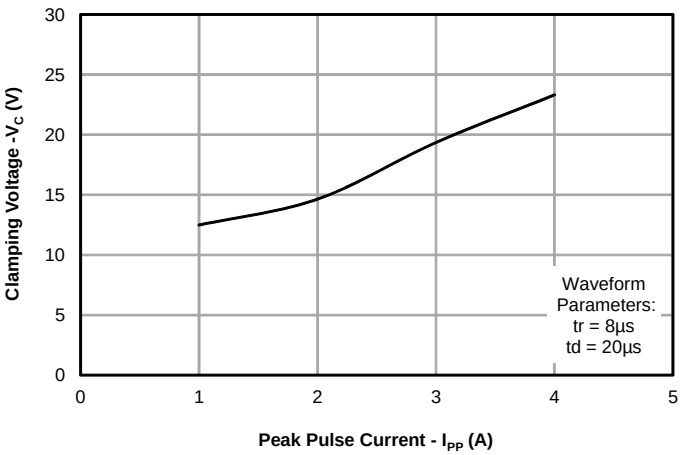
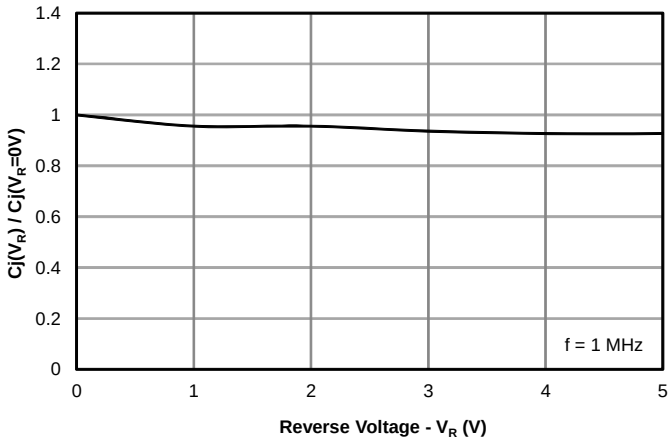
Typical Characteristics

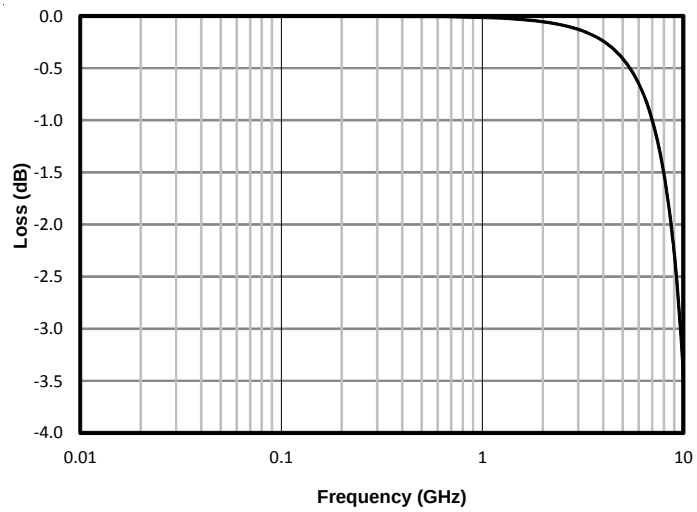
Clamping Voltage vs. Peak Pulse Current



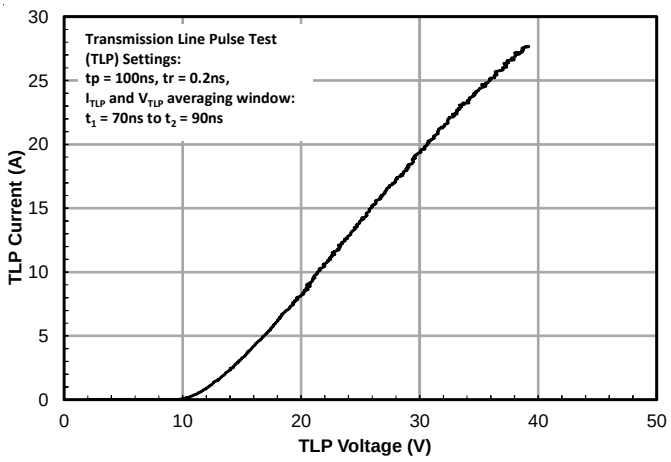
Typical Capacitance vs. Reverse Voltage



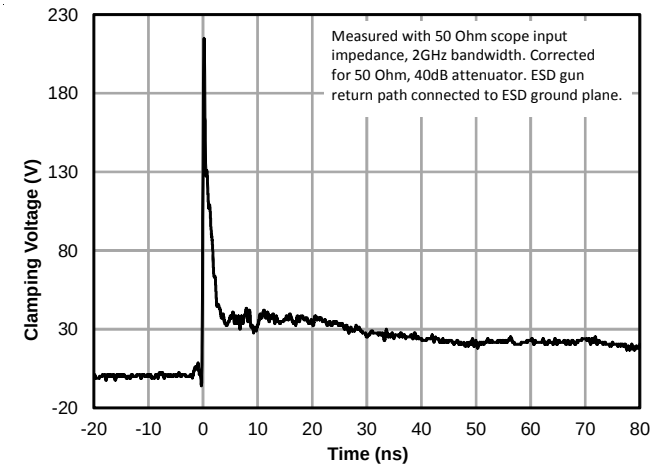
Typical Insertion Loss (S21)



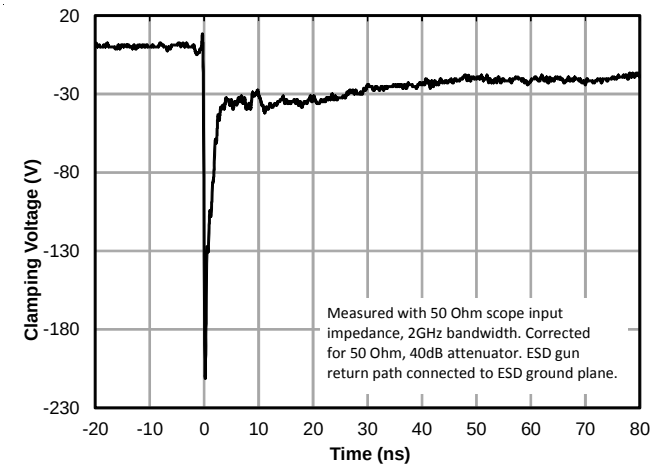
TLP Characteristic



ESD Clamping (+8kV Contact per IEC 61000-4-2)



ESD Clamping (-8kV Contact per IEC 61000-4-2)



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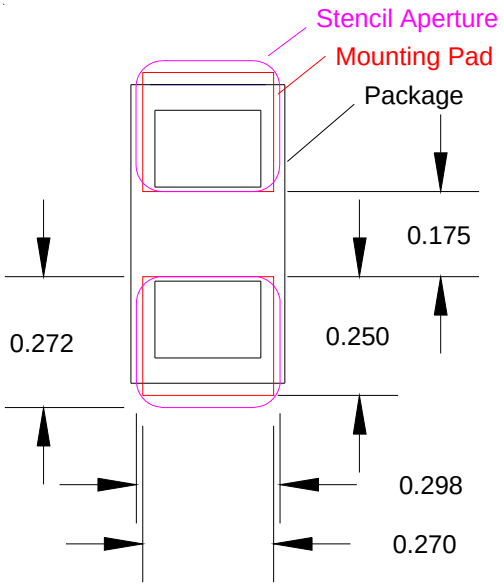
Applications Information

Assembly Guidelines

The small size of this device means that some care must be taken during the mounting process to insure reliable solder joint. The table below provides Semtech's recommended assembly guidelines for mounting this device. The figure at the right details Semtech's recommended aperture based on the below recommendations. Note that these are only recommendations and should serve only as a starting point for design since there are many factors that affect the assembly process. The exact manufacturing parameters will require some experimentation to get the desired solder application.

Assembly Parameter	Recommendation
Solder Stencil Design	Laser cut, Electro-polished
Aperture shape	Rectangular with rounded corners
Solder Stencil Thickness	0.100 mm (0.004")
Solder Paste Type	Type 4 size sphere or smaller
Solder Reflow Profile	Per JEDEC J-STD-020
PCB Solder Pad Design	Non-Solder mask defined
PCB Pad Finish	OSP OR NiAu

Recommended Mounting Pattern



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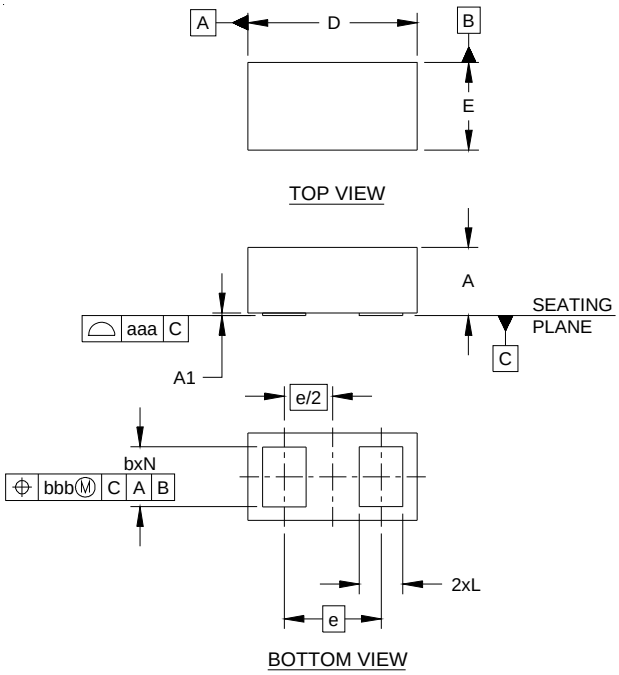
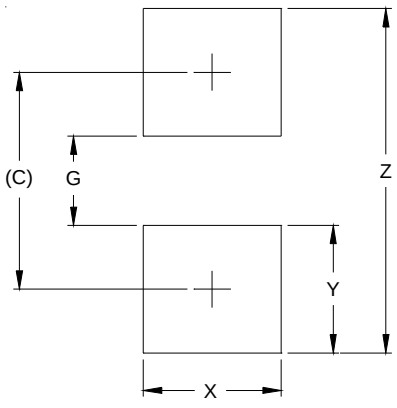
Outline Drawing - SLP0603P2X3



DIMENSIONS			
DIM	MILLIMETERS		
	MIN	NOM	MAX
A	0.235	0.25	0.265
A1	0.000	0.02	0.025
b	0.20	0.22	0.24
D	0.585	0.62	0.655
E	0.285	0.32	0.355
e	0.355 BSC		
L	0.14	0.16	0.18
N	2		
aaa	0.08		
bbb	0.10		

NOTES:
1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

Land Pattern - SLP0603P2X3



DIMENSIONS	
DIM	MILLIMETERS
C	(0.425)
G	0.175
X	0.270
Y	0.250
Z	0.675

NOTES:
1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

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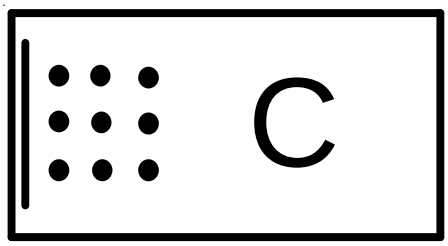
Marking Code



Notes:
1)Dots represent date code matrix

Ordering Information

Ordering Number	Qty per Reel	Carrier Tape	Reel Size	Comments
RClamp0521Z.TNT	10,000	Plastic	7 Inch	Not Recommended for New Designs
RClamp0521Z.TFT	15,000	Paper	7 Inch	

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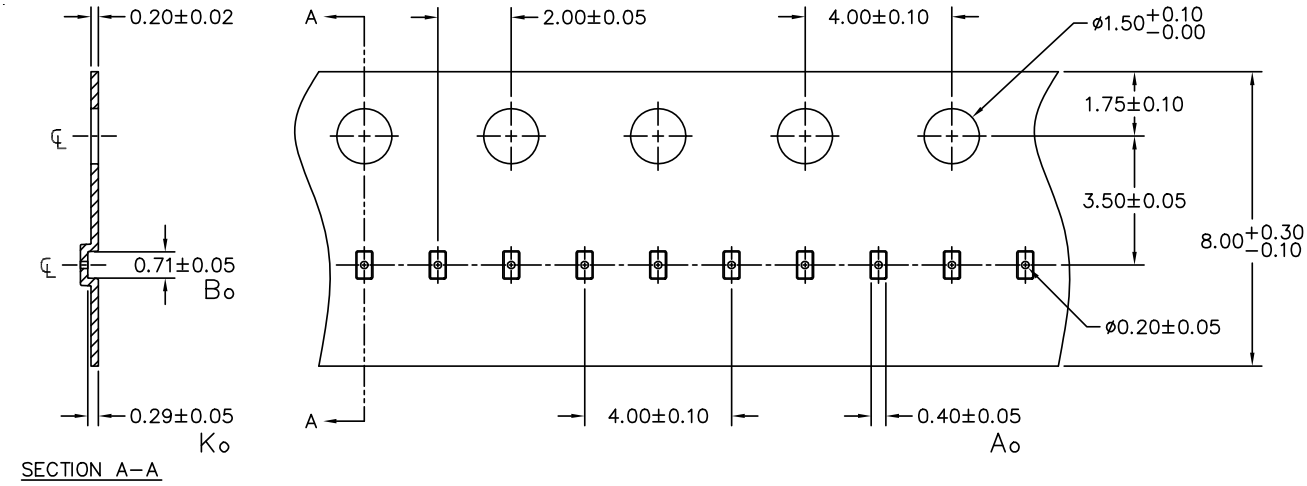
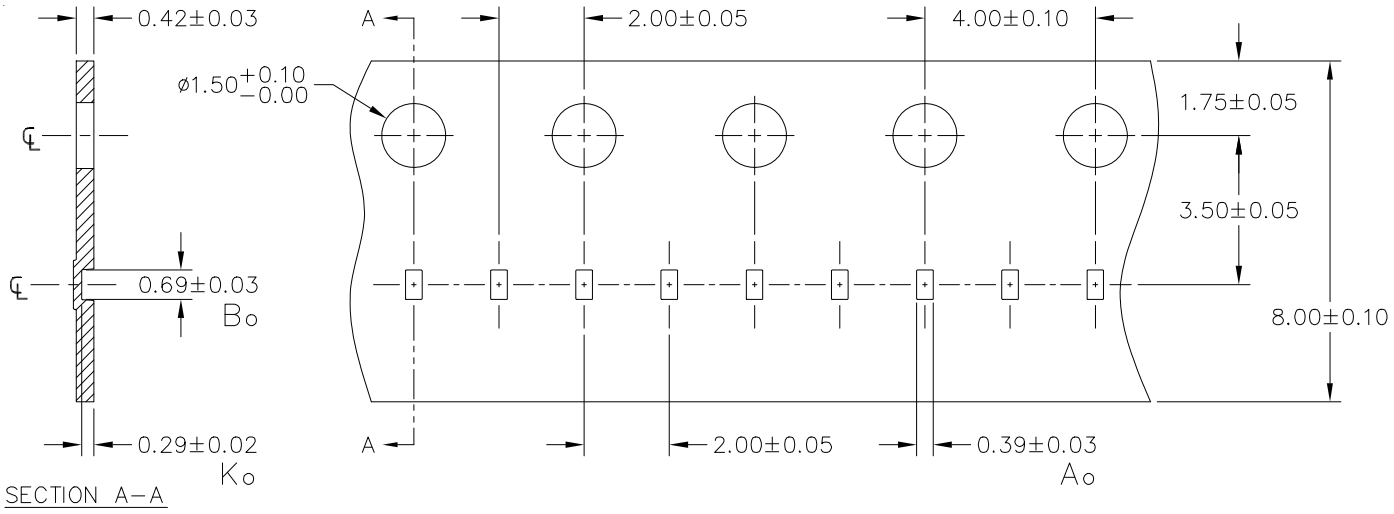
Carrier Tape Specification

Plastic Tape



NOTES: 1.) ALL DIMENSIONS IN MILLIMETERS UNLESS OTHERWISE SPECIFIED.

Paper Tape



Note: All dimensions in mm unless otherwise specified

Device Orientation in Tape